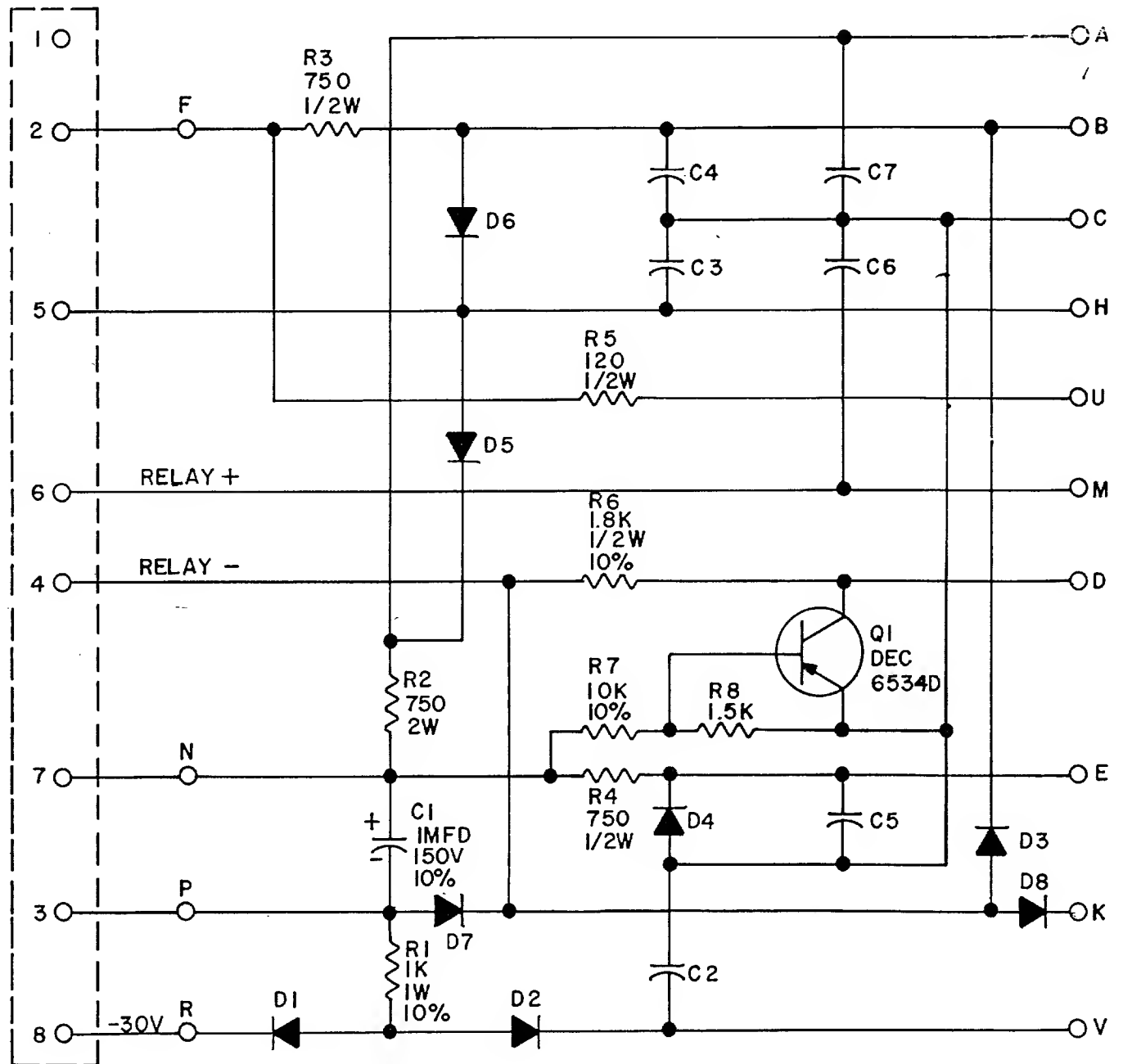


THIS SCHEMATIC IS FURNISHED ONLY FOR TEST AND MAINTENANCE PURPOSES. THE CIRCUITS ARE PROPRIETARY IN NATURE AND SHOULD BE TREATED ACCORDINGLY. COPYRIGHT 1969 BY DIGITAL EQUIPMENT CORPORATION

TYPICAL PIN ASSIGNMENTS			
PIN	8	8 I, 8 L	DC08
A	+10V	+5V	+5V
B	-15V	-15V	+5V
C	GND	GND.	GND
H	LOGIC INPUT	LOGIC INPUT	—
U	—	—	LOGIC INPUT
M	READER ENABLE	READER ENABLE	READER ENABLE
D	LOGIC OUTPUT	—	—
E	—	LOGIC OUTPUT	LOGIC OUTPUT
K	—	—	-15V
V	-30V OPTIONAL	-30V OPTIONAL	-30V OPTIONAL

UNLESS OTHERWISE INDICATED:
CAPACITORS ARE .01MFD 100V, 20%
DIODES ARE D671
RESISTORS ARE 1/4W, 5%
CONNECTOR IS DEC# 1209456

AMP
CONNECTOR



REVISIONS	CHK	CHG NO.	REV.	DRN.	DATE	TRANSISTOR & DIODE CONVERSION CHART				<div>digital</div> <div>EQUIPMENT CORPORATION</div> <div>MAYNARD, MASSACHUSETTS</div>	TITLE			
	CHK	CHG NO.	REV.	CHK'D	DATE	DEC	EIA	DEC	EIA		TELETYPE CONNECTOR W078			
	CHK	CHG NO.	REV.	CHK'D	DATE	D671	1N3653				SIZE	CODE	NUMBER	REV.
	CHK	CHG NO.	REV.	CHK'D	DATE	DEC6534D	MPS6534D				B	CS	W078-0-1	
				PROD.	DATE					PRINTED CIRCUIT REV.				